

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Prior Application: Y. KANNO et al
Serial No. 10/325,920
Filed: December 23, 2002

Group Art: 2818
Examiner: G. Auduong
For: SEMICONDUCTOR INTEGRATED CIRCUIT

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

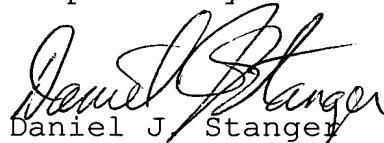
June 27, 2003

Sir:

In accordance with the duty of disclosure, the Applicants inform the Examiner of the documents cited during prosecution of the parent application, USSN 10/325,920.

The Applicants request the Examiner to initial and return a copy of the attached PTO-1449 form as an indication that the references have been considered.

Respectfully submitted,



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Registration No. 32,846
Attorney for Applicant(s)

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Date: June 27, 2003

FORM PTO-1449 (REV. 7-80)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. ASA-994-03	SERIAL NO.		
LIST OF DOCUMENTS CITED BY APPLICANT <i>(Use several sheets if necessary)</i>				APPLICANT Y. KANNO et al			
				FILING DATE 06/27/03		GROUP 2818	
U.S. PATENT DOCUMENTS							
* EXAMINER INITIAL		DOCUMENT	DATE	NAME	CLASS	SUBCLASS	FILING DATE <i>(If Appropriate)</i>
	AA	4,803,664	02/07/89	Itoh			
	AB	4,879,682	11/07/89	Engles			
	AC	3,593,037	03/1970	Hoff, Jr.			
	AD	3,706,079	09/1971	Vadasz et al			
	AE	4,893,278	01/1990	Ito			
	AF	6,016,268	01/2000	Worley			
	AG	6,219,277	04/2001	Devin et al			
	AH	3,706,891	12/1972	Donofrio et al			
	AI	4,084,108	04/1978	Fujimoto			
	AJ	4,945,393	07/1990	Beltram et al			
	AK	5,396,452	03/1995	Wahlstrom			
FOREIGN PATENT DOCUMENTS							
		DOCUMENT	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATIO YES NO
	AL	11-134866	05/21/99	Japan (with English Abstract)			<input type="checkbox"/> <input type="checkbox"/>
	AM						<input type="checkbox"/> <input type="checkbox"/>
	AN						<input type="checkbox"/> <input type="checkbox"/>
	AO						<input type="checkbox"/> <input type="checkbox"/>
	AP						<input type="checkbox"/> <input type="checkbox"/>
OTHER DOCUMENTS <i>(Including Author, Title, Date, Pertinent Pages, etc.)</i>							
	AR	K. Ito, "Very Large Scale Integration Memory", Baifukan Co., Ltd.,					
		1994, pp. 12-17.					
	AS						
	AT						
EXAMINER				DATE CONSIDERED			
<small>* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form next communication to applicant.</small>							

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U.S. PATENT DOCUMENTS

* EXAMINER INITIAL	DOCUMENT	DATE	NAME	CLASS	SUBCLASS	FILING DATE <i>(If Appropriate)</i>
	AA	5,656,528	08/1997	Wahlstrom		
	AB	6,519,195	02/2003	Kanno et al		
	AC					
	AD					
	AE					
	AF					
	AG					
	AH					
	AI					
	AJ					
	AK					

FOREIGN PATENT DOCUMENTS

DOCUMENT	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATIO	
					YES	NO
AL					<input type="checkbox"/>	<input type="checkbox"/>
AM					<input type="checkbox"/>	<input type="checkbox"/>
AN					<input type="checkbox"/>	<input type="checkbox"/>
AO					<input type="checkbox"/>	<input type="checkbox"/>
AP					<input type="checkbox"/>	<input type="checkbox"/>

OTHER DOCUMENTS *(Including Author, Title, Date, Pertinent Pages, etc.)*

	AR		
	AS		
	AT		

EXAMINER	DATE CONSIDERED
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